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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/073,733	02/11/2002	Bruce C. McDermott	83552PCW	5189	
7590 02/10/2005			EXAMINER		
Thomas H. Cl	lose	NGUYEN, LUONG TRUNG			
Patent Legal St Eastman Kodal		ART UNIT	PAPER NUMBER		
343 State Stree		2612			
Rochester, NY	14650-2201	DATE MAILED: 02/10/2005			

Please find below and/or attached an Office communication concerning this application or proceeding.

		Appl	ication No.	Applicant(s)				
			73,733	MCDERMOTT E	MCDERMOTT ET AL.			
Office Action Summary		Exan	niner	Art Unit	Τ.			
		. LUOI	NG T NGUYEN	2612				
 Period for	The MAILING DATE of this commu Reply	nication appears o	n the cover sheet v	vith the correspondence a	ddress			
A SHOP THE MA - Extension after SI2 - If the pe - If NO pe - Failure I Any repi	RTENED STATUTORY PERIOD F AILING DATE OF THIS COMMUN ons of time may be available under the provisions K (6) MONTHS from the mailing date of this com- priod for reply specified above is less than thirty (is period for reply is specified above, the maximum so to reply within the set or extended period for reply ty received by the Office later than three months patent term adjustment. See 37 CFR 1.704(b).	ICATION. s of 37 CFR 1.136(a). In nunication. 30) days, a reply within the tatutory period will apply of will, by statute, cause the	no event, however, may a ne statutory minimum of th and will expire SIX (6) MC ne application to become A	reply be timely filed irty (30) days will be considered time INTHS from the mailing date of this ABANDONED (35 U.S.C. § 133).				
Status								
1)□ R	esponsive to communication(s) file	ed on .		,				
		2b)⊠ This action	is non-final.					
-	<del>, _</del>							
Disposition	n of Claims							
4a 5)□ C 6)⊠ C 7)□ C	laim(s) <u>1-8</u> is/are pending in the and of the above claim(s) is/at laim(s) is/at allowed. laim(s) <u>1-8</u> is/are rejected. laim(s) is/are objected to. laim(s) are subject to restrict	re withdrawn fron						
Application	n Papers							
10)⊠ Th Ap Re	ne specification is objected to by the drawing(s) filed on 11 February oplicant may not request that any objected to oath or declaration is objected to	2002 is/are: a)⊠ ction to the drawing the correction is re	g(s) be held in abeya equired if the drawing	nnce. See 37 CFR 1.85(a). g(s) is objected to. See 37 C	FR 1.121(d).			
Priority und	der 35 U.S.C. § 119				÷			
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No.</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>								
Attachment(s)			<b></b> □	9 (970				
2) Notice o	f References Cited (PTO-892) f Draftsperson's Patent Drawing Review (F ion Disclosure Statement(s) (PTO-1449 or o(s)/Mail Date		Paper No	Summary (PTO-413) (s)/Mail Date Informal Patent Application (PTo	O-152)			

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#### **DETAILED ACTION**

# Claim Objections

1. Claims 1-4, 6 are objected to because of the following informalities:

Claim 1 (line 2), "substrate" should be changed to --substrate;--.

Claim 2 (lines 1-2), "the programmable memory" should be changed to --the non-volatile, programmable memory--.

Claim 4 (lines 2), "the non-volatile memory" should be changed to --the non-volatile, programmable memory--.

Claim 6 (line 2), "the non-volatile programmable memory" should be changed to --the non-volatile, programmable memory--.

Appropriate correction is required.

## Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

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3. Claims 1-8 are rejected under 35 U.S.C. 102(e) as being anticipated by Schimdt (US 6,278,481).

Regarding claim 1, Schmidt discloses 1. An image capture device for capturing an image comprising a substrate (a single chip, Figure 5, Column 10, Lines 25-30, 49-55); an image sensor (CMOS imager 505, Figure 5, Column 10, Lines 25-39) on the substrate for receiving incident light; non-volatile, programmable memory (EEPROM program memory 520, Figure 5, Column 10, Lines 39-48) on the substrate for storing predetermined variables (instructions) that are loaded into predetermined circuitry used to manage the image sensor upon startup.

Regarding claim 2, Schmidt discloses the programmable memory is PROM, FLASH or BPROM for providing non-volatile memory that keeps its contents when power is removed (EEPROM program memory 520, Figure 5, Column 10, Lines 39-48).

Regarding claim 3, Schmidt discloses the image sensor and integrally disposed programmable memory are components of a digital still camera (digital camera, See abstract, Figure 5).

Regarding claim 4, Schmidt discloses dedicated logic (microcontroller 510, Figure 5, Column 10, Lines 40-48) for loading the non-volatile memory prior to image capture.

Claims 5-8 are method claims of apparatus claims 1-4, respectively. Therefore, see Examiner's comments regarding claims 1-4, respectively, except for the step of "providing a

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microprocessor for assisting the image sensor in image capture" which is disclosed as microcontroller 510 in Schmidt (Figure 5, Column 10, Lines 40-48).

### Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Heller et al. (US 6,396,539) discloses CMOS imaging device with integrated defective pixel correction circuitry.

Lee et al. (US 6,721,008) discloses integrated CMOS active pixel digital camera.

Hsieh et al. (US 6,757,019) discloses low-power parallel processor and imager having peripheral control circuitry.

Schinner (US 2003/0052984) discloses user selection of power-on configuration.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to LUONG T NGUYEN whose telephone number is (703) 308-9297 or (571) 272-7315. The examiner can normally be reached on 7:30AM - 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wendy Garber can be reached on (703) 305-4929 or (571) 272-7308. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

LN LN 01/26/05

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